1/6 F I G. 1 A

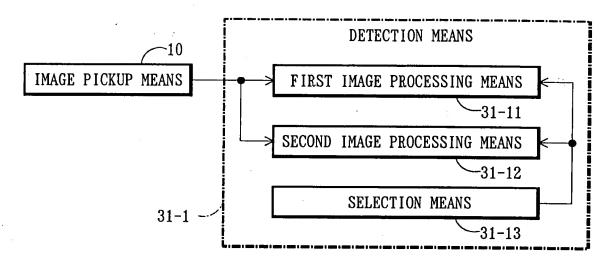


FIG. 1B

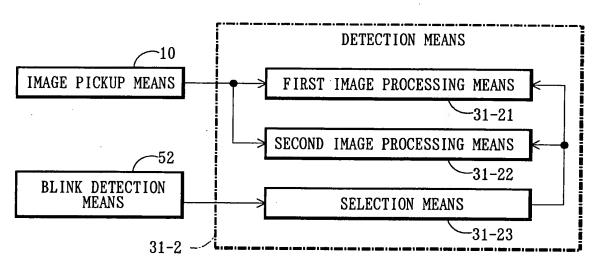
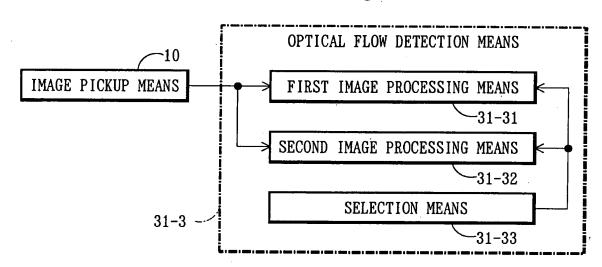
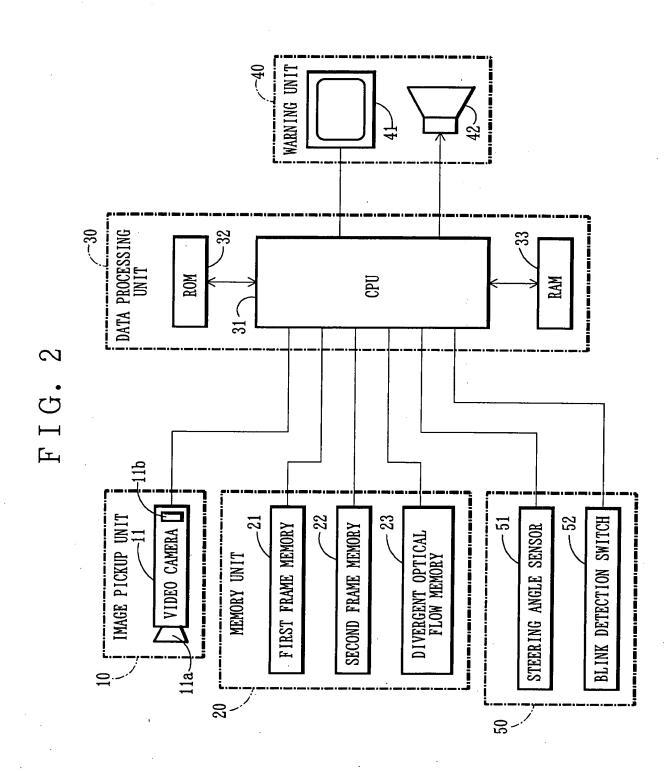
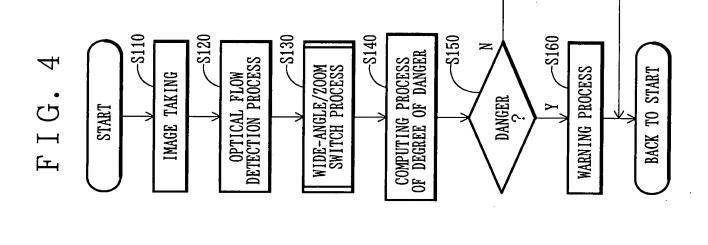
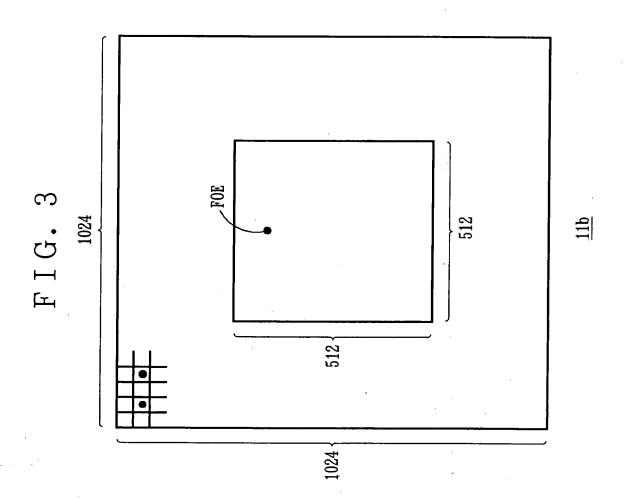


FIG. 1C









F I G. 5

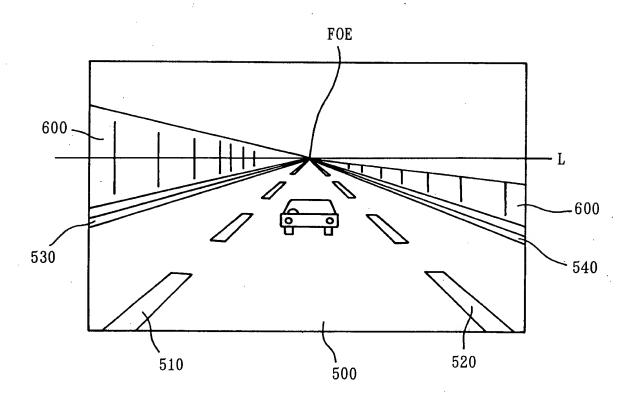
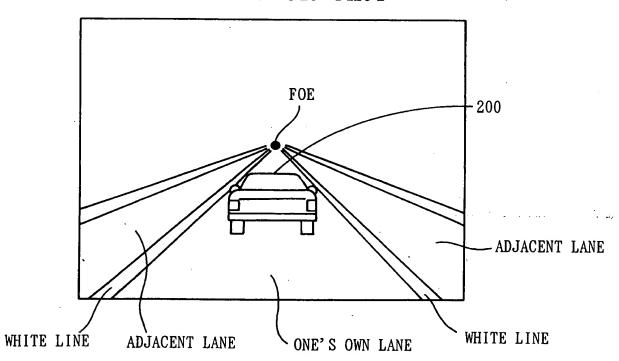
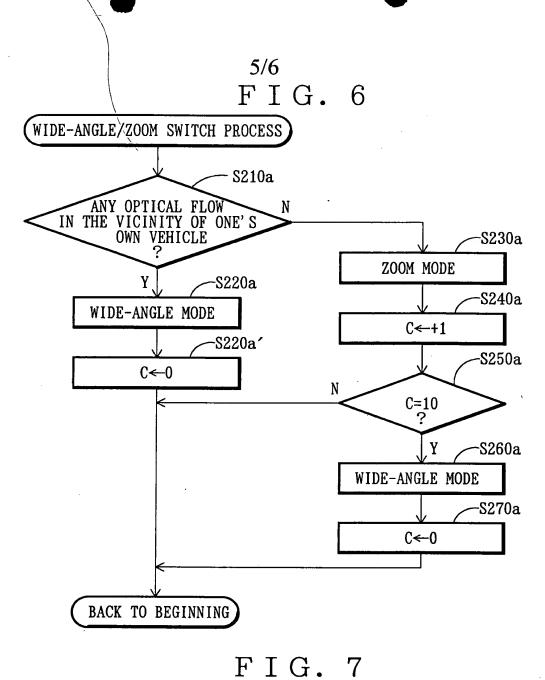


FIG. 10 PRIOR ART





PRIOR ART

-30

DATA PROCESSING UNIT

ROM

11a

11b

11b

11b

RAM

